

	L #	Hits	Search Text	DBs
1	L1	3615	(361/734,738,760,763,7 67,768,782,783,792-795).ccls.	USP AT; US-P GPU B
2	L2	824	(361/306.1,306.2,306.3).ccls.	USP AT; US-P GPU B
3	L3	564	361/803.cccls.	USP AT; US-P GPU B
4	L4	1111	428/210.cccls.	USP AT; US-P GPU B
5	L5	4542	(257/686,691,700,723,7 24,777).cccls.	USP AT; US-P GPU B
6	L6	9151	1 3 4 5	USP AT; US-P GPU B
7	L7	1415	6 and @pd>=20030205	USP AT; US-P GPU B

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L8</b>	<b>411</b>	<b>7 and (capacitor\$1 condenser\$1 (capacitive adj element\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>9</b>	<b>L9</b>	<b>120</b>	<b>2 and @pd&gt;=20030205</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>117</b>	<b>9 and (capacitor\$1 condenser\$1 (capacitive adj element\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>524</b>	<b>8 10</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>152</b>	<b>11 and (through\$1hole\$1 (via adj hole\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>372</b>	<b>11 not 12</b>	<b>USP AT; US-P GPU B</b>